

# Mixed Signal ASICs: From design to good integrated circuits

Workshop on Affordable Design and Production of Mixed Signal ASICs  
for Small and Medium Enterprises (SMEs)

## Abstract

When the engineer has finished the design work and the simulation delivers correct results, some people still think that the work is done. In reality, many other aspects like test methodology, package selection, process tolerances and many more have a strong influence on the product performance and price in volume production.

Considering these aspects right from the beginning can save both, time and money. In this seminar, experts share their experiences and would also like to listen to and discuss yours.

**Dates:** May 17th-18th 2011

**Pricing:** Option 1: Day 1 presentations only - CHF 175.-  
Option 2: Day 1 plus cultural & social events - CHF 250.-  
Option 3: Full program (Day 1&2) - CHF 350.-

**Venue:** Fachhochschule Nordwestschweiz, Klosterzelgstrasse 2, CH-5210 Windisch

**For detailed information and registration please go to [www.ms-asics.ch](http://www.ms-asics.ch).**

## Day 1: Presentations and Workshop

- IME [www.fhnw.ch/ime](http://www.fhnw.ch/ime)
- Aptasic [www.aptasic.ch](http://www.aptasic.ch)
- D4T Systems [www.d4t-systems.com](http://www.d4t-systems.com)
- Serma Technologies [www.serma-technologies.com](http://www.serma-technologies.com)
- EDA Solutions [www.eda-solutions.com](http://www.eda-solutions.com)

## Day 2: Hands-on Training

- (Tanner EDA [www.tannereda.com](http://www.tannereda.com))
- Schematic Entry
- Simulation
- IC Layout
- DRC, LVS

In collaboration with